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PATENT APPLICATION

ATTORNEY DOCKET NO. 200312082-1

Inventor(s): Yong V. Wang et al

Confirmation No.: 8067

Application No.: 10/748964

Examiner: Unknown

Filing Date: Dec 30, 2003

Group Art Unit: 2838

Title: System And Method For Integrating Subcircuit Models In An Integrated Power Grid Analysis
Environment

Commissioner For Patents
PO Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRYMore than 25 months have passed since:

- ☒ **NEW APPLICATIONS**
The filing of this application on Dec 30, 2003. No communication has been received from the Patent and Trademark Office indicating action on this application.
- ☐ **AMENDED APPLICATIONS**
The filing of a response on _____. No communication has been received from the Patent and Trademark Office.
- ☐ **APPEALED APPLICATION**
The Appeal Brief was filed on _____.
☐ An Examiner's Answer was mailed on _____.
☐ A Reply Brief to the Examiner's Answer was submitted on _____.
- ☐ **ALLOWED APPLICATIONS**
The mailing of FORM POL-85 and/or Examiner's Amendment on _____.

Kindly advise the undersigned of the present status of this application.

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Respectfully submitted,

By


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1-25-06

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